

Figure 1

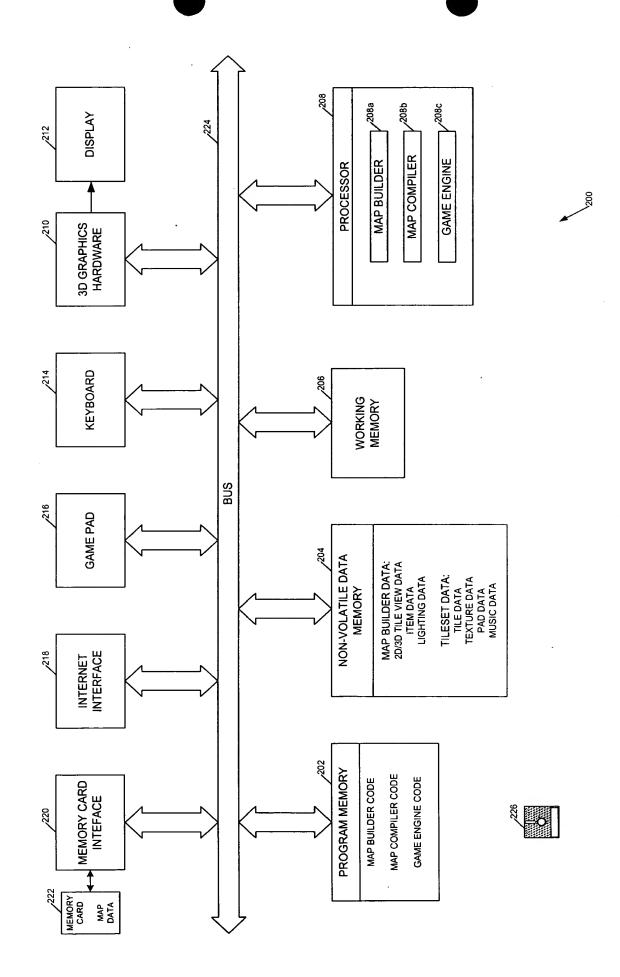
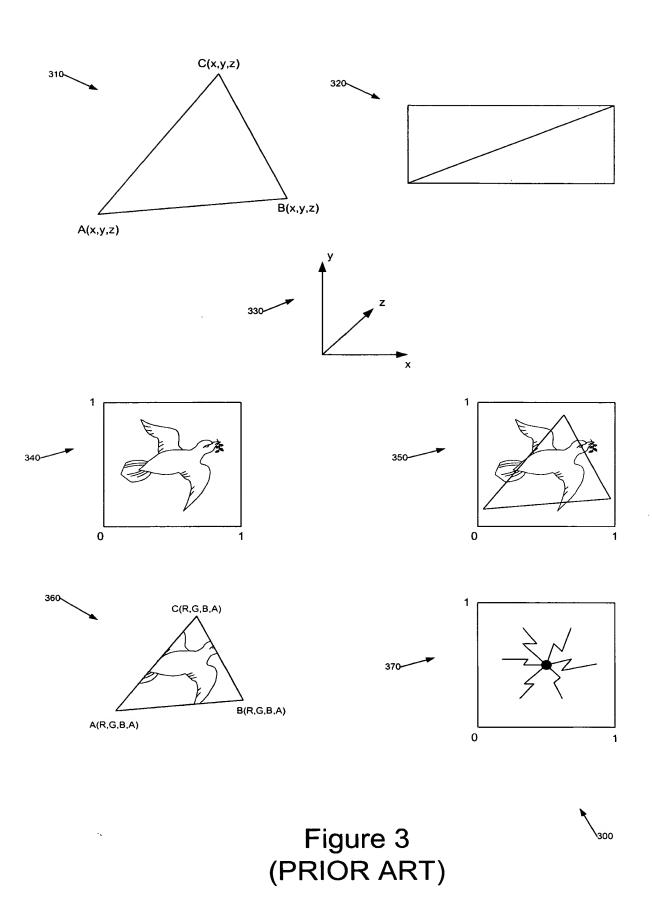


Figure 2



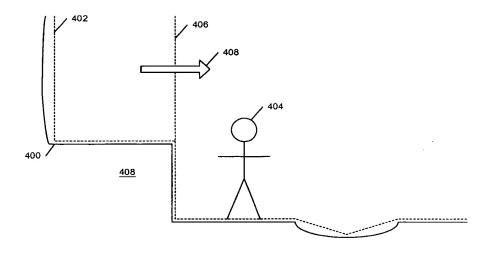


Figure 4a

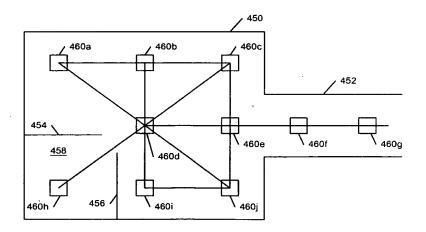
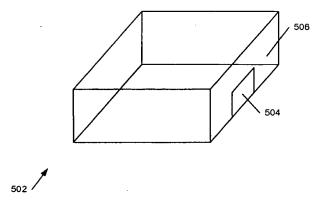


Figure 4b (PRIOR ART)



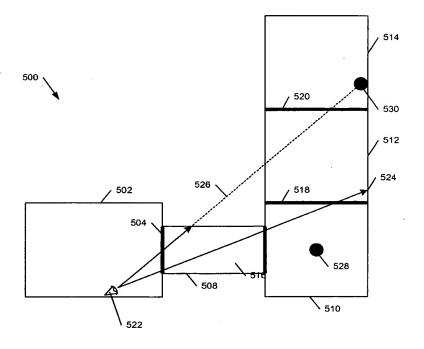


Figure 5

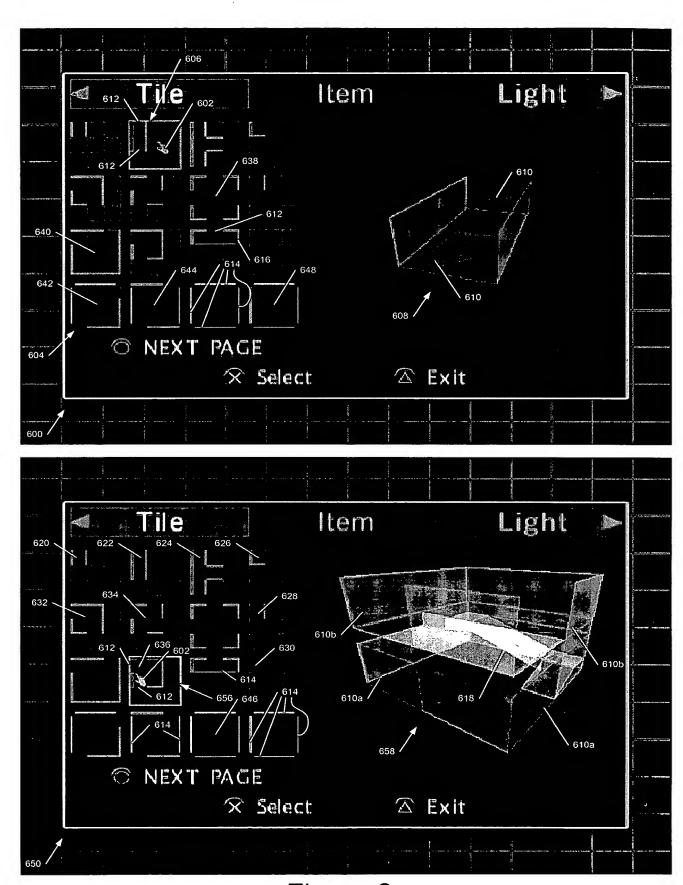


Figure 6a

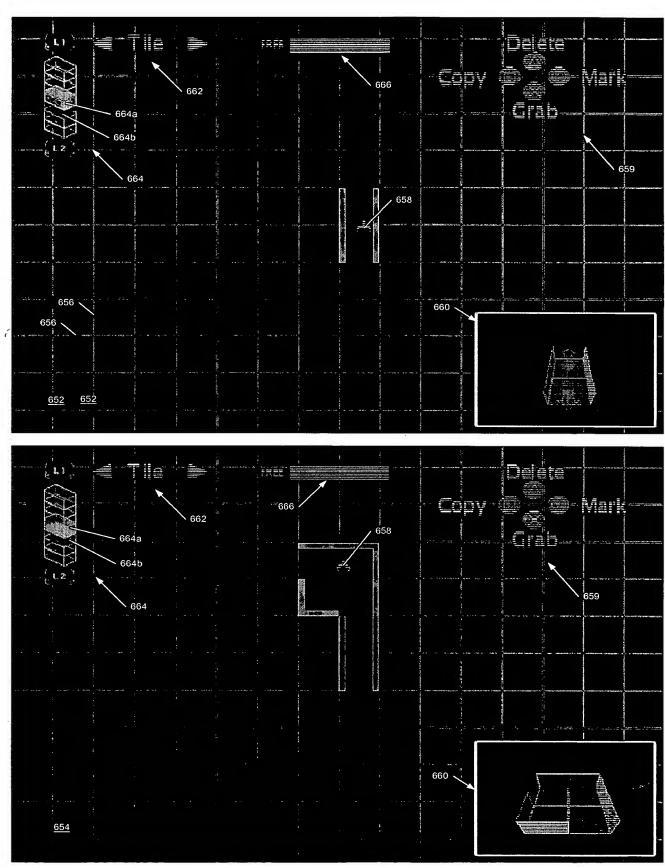


Figure 6b

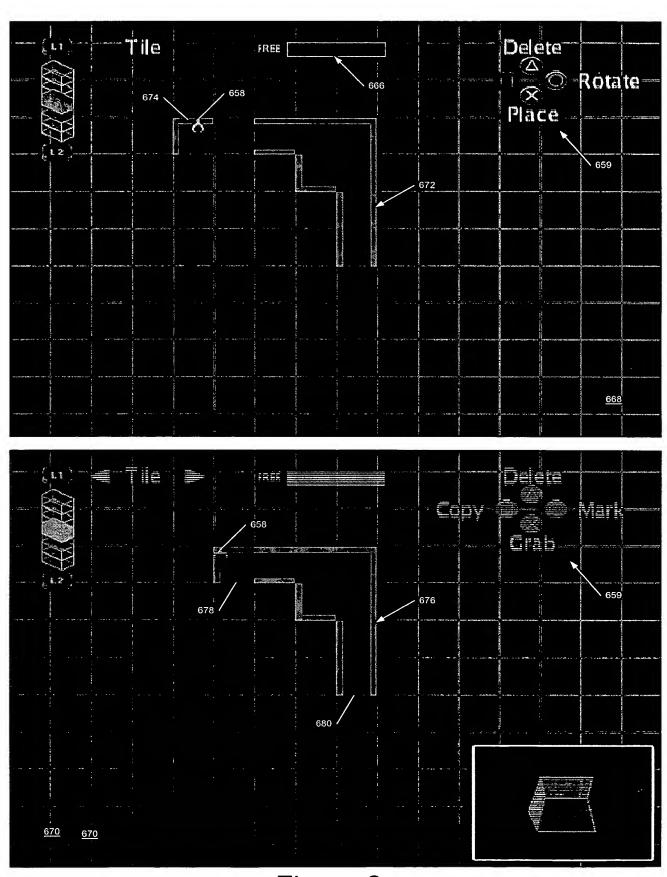


Figure 6c

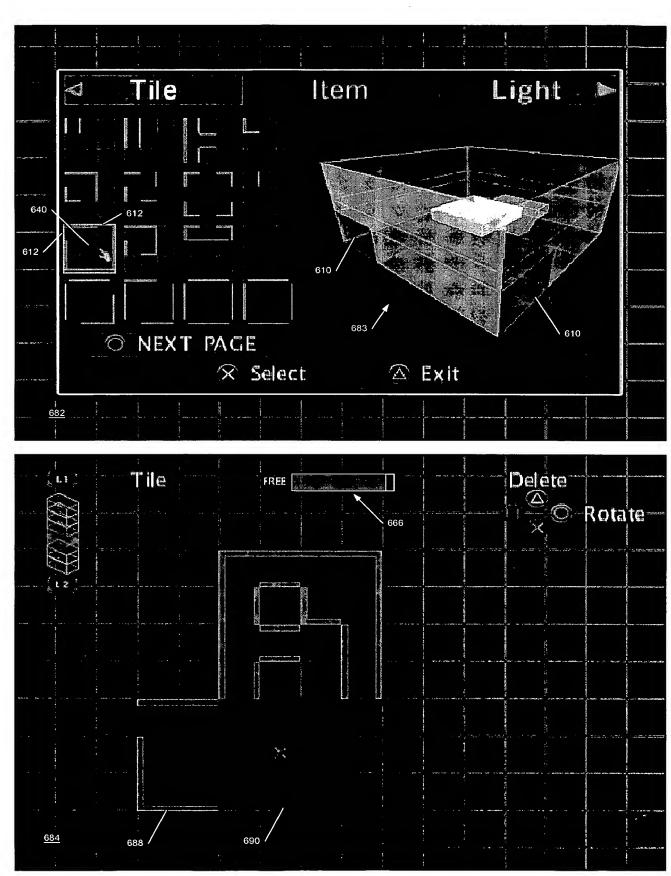


Figure 6d

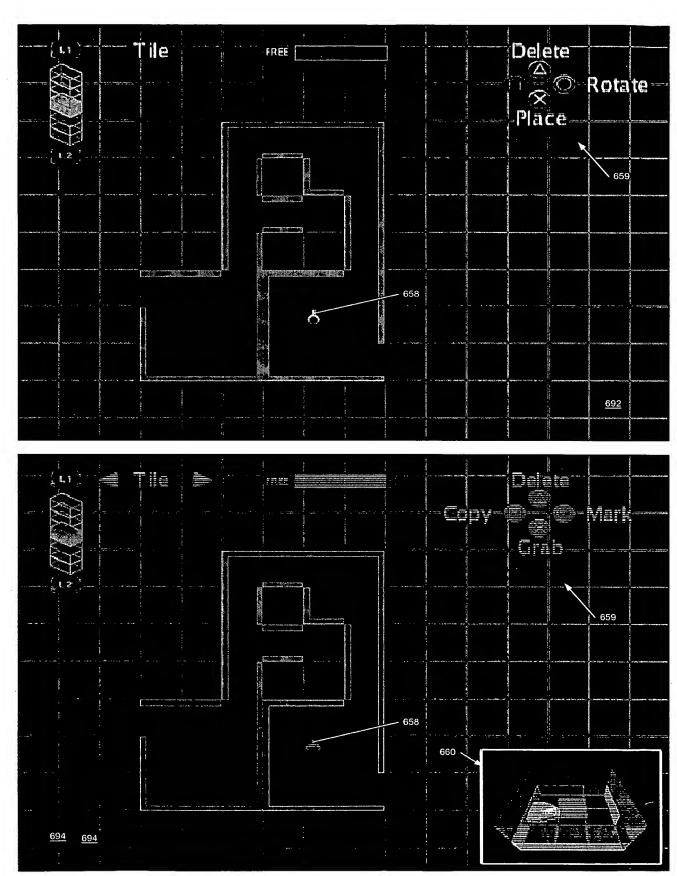
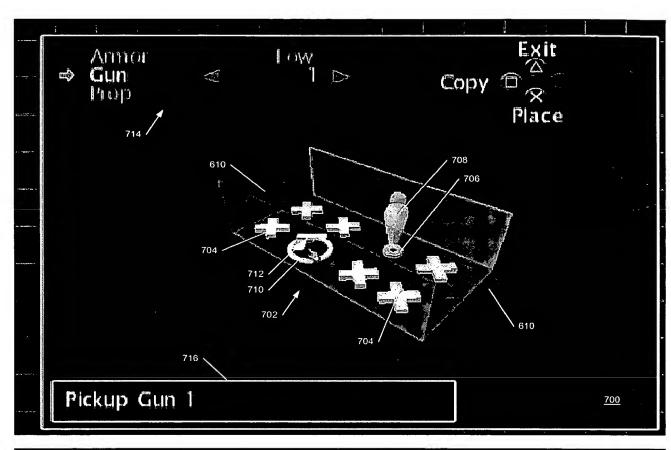


Figure 6e



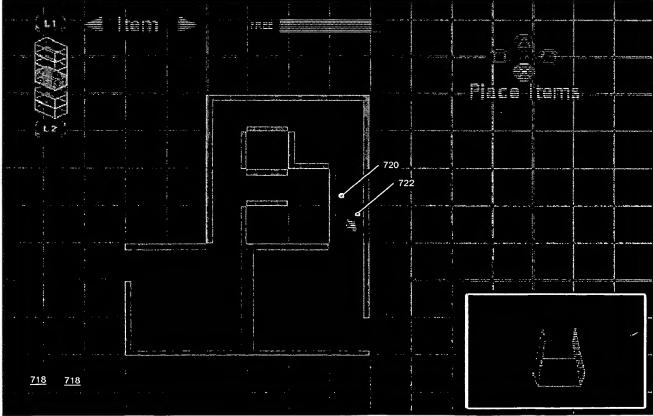


Figure 7a

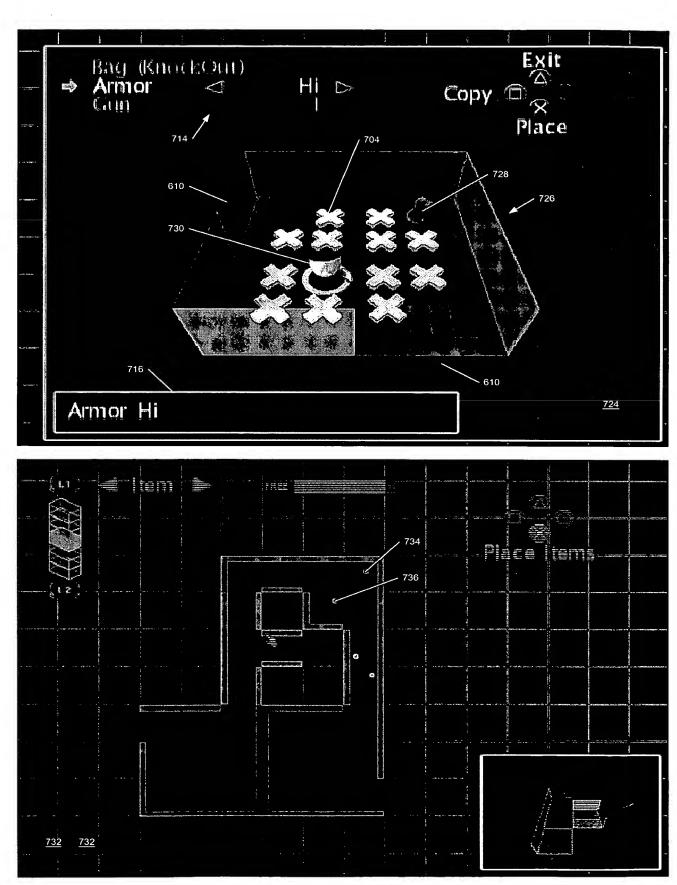
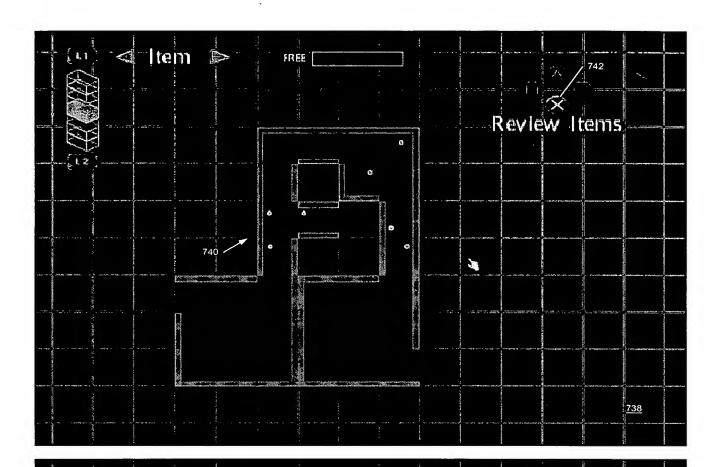


Figure 7b



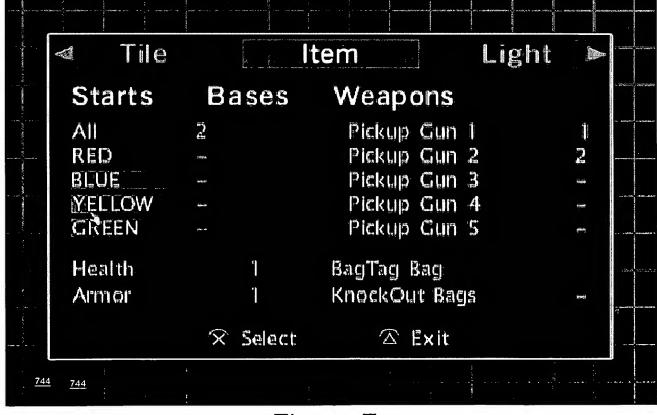
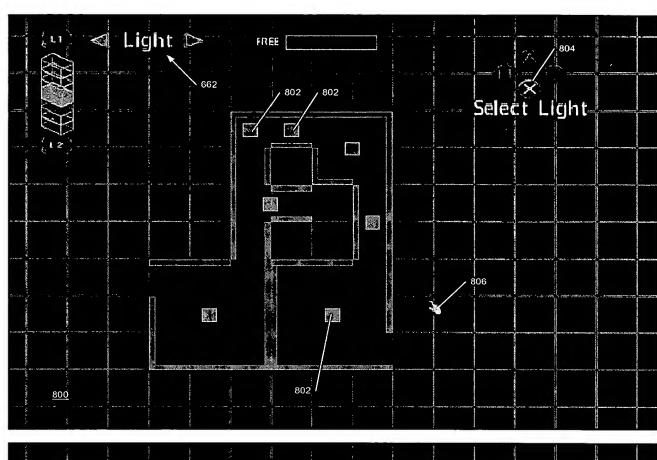


Figure 7c



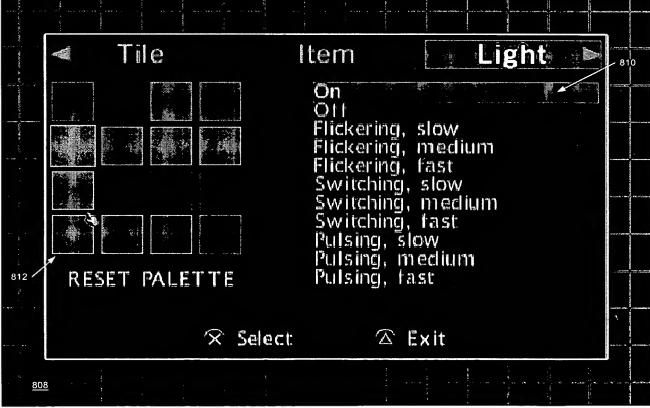


Figure 8a

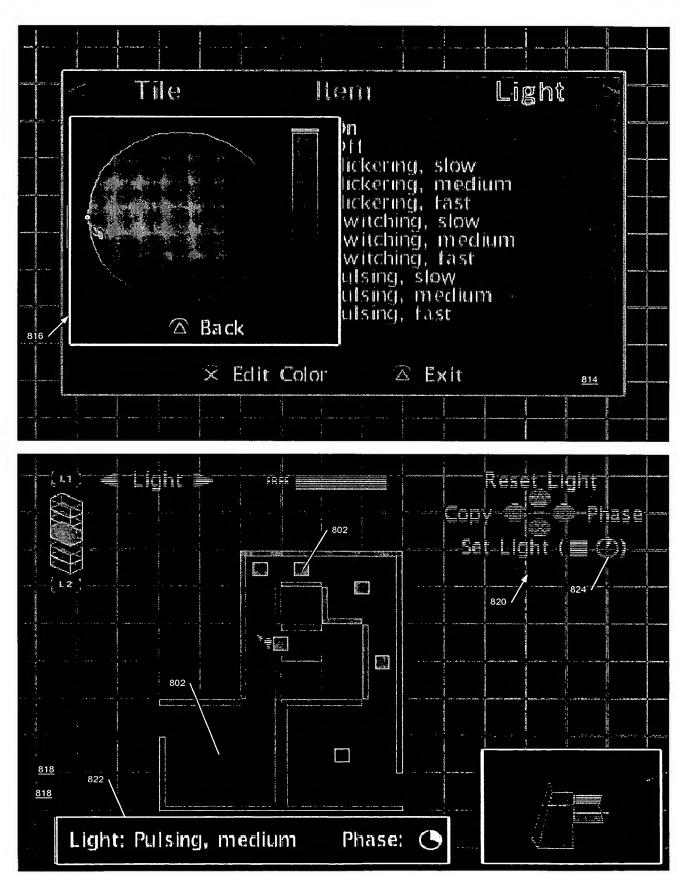
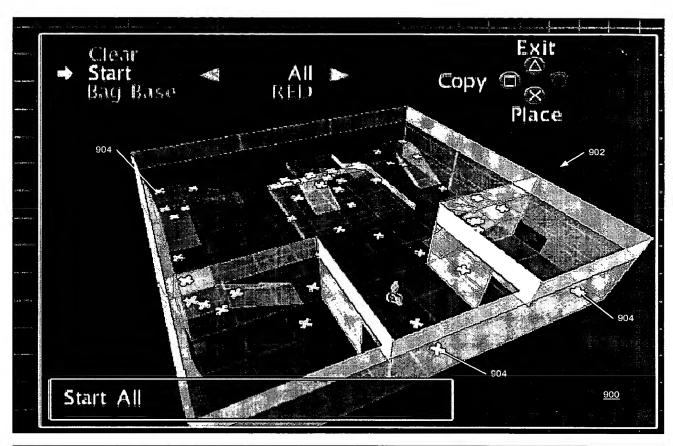


Figure 8b



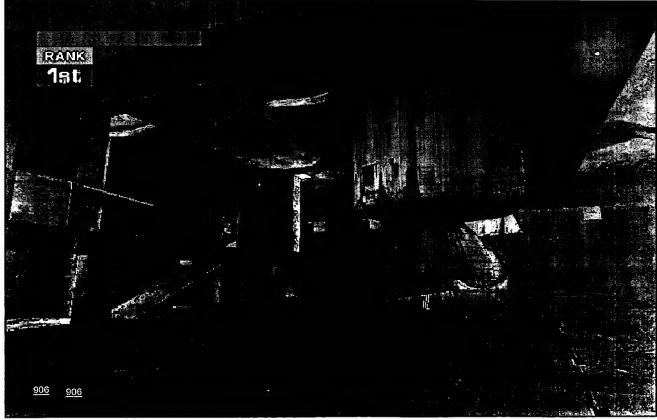
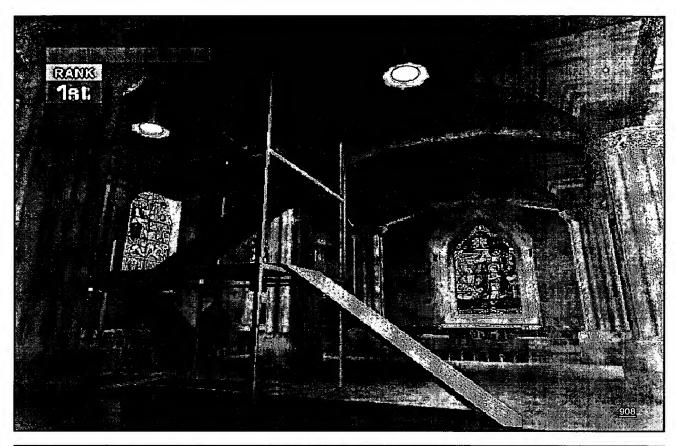


Figure 9a



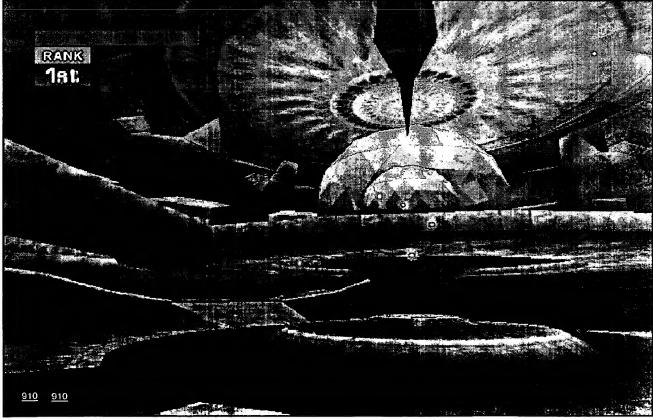
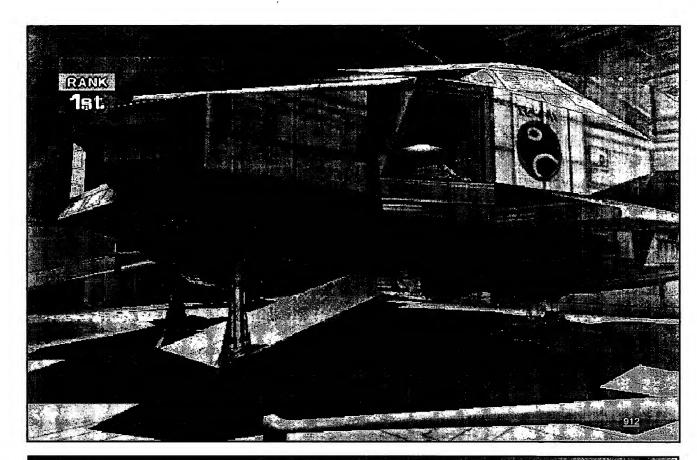


Figure 9b



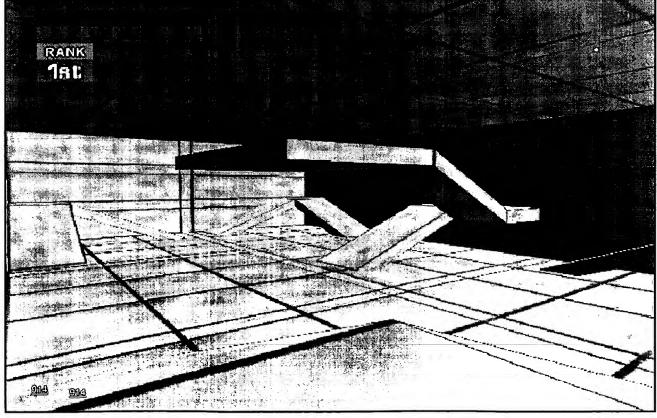


Figure 9c

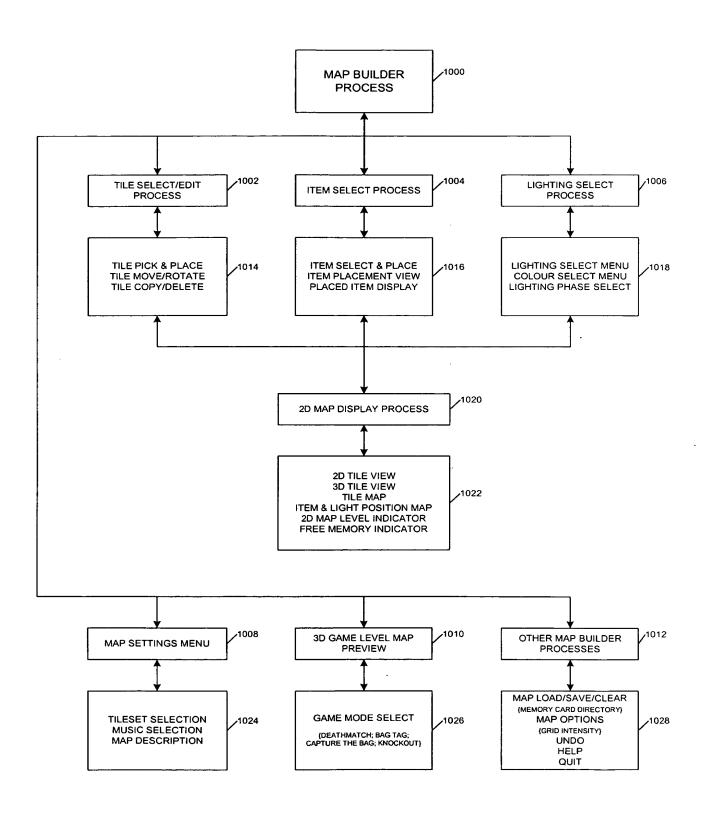


Figure 10

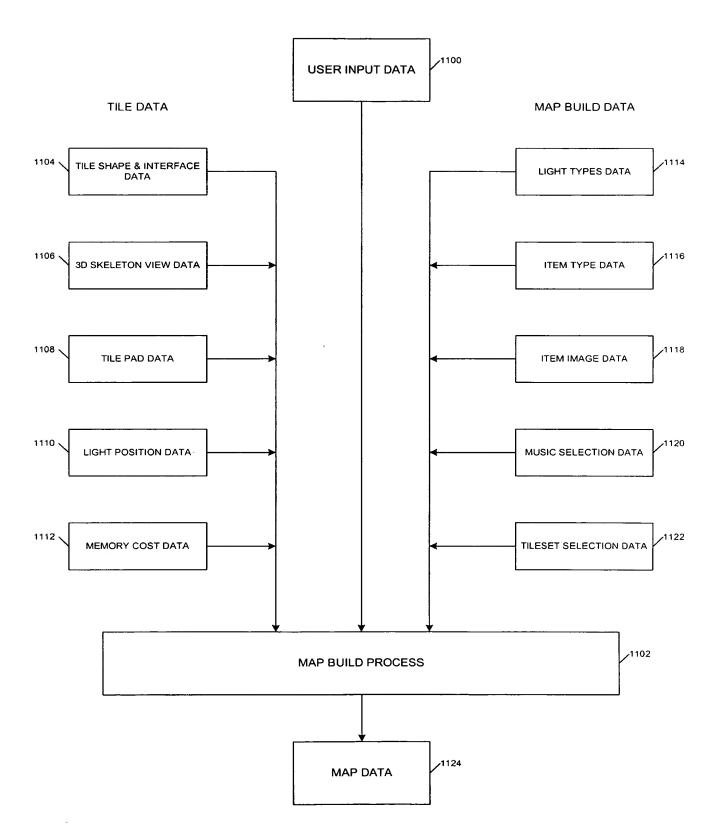


Figure 11

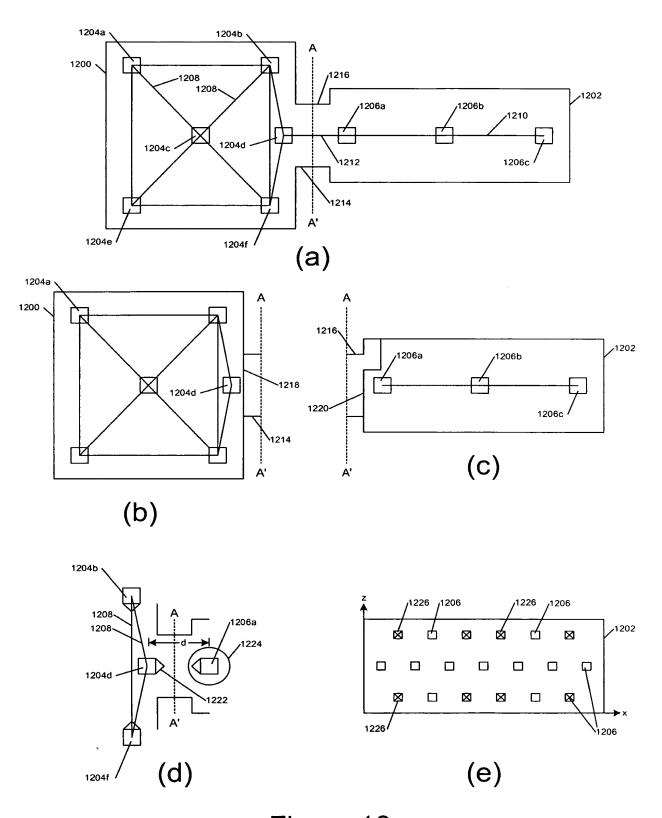


Figure 12

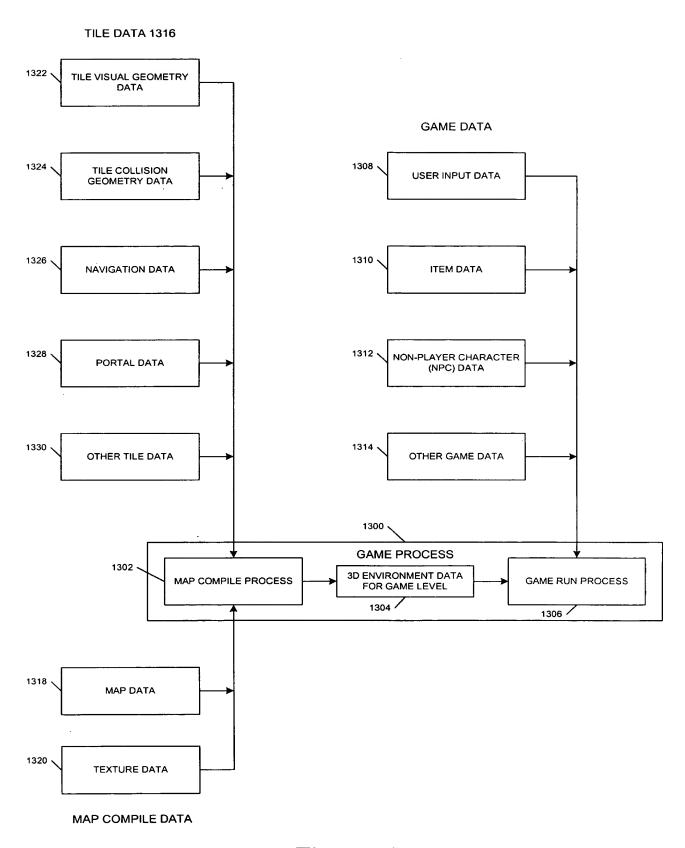


Figure 13

double_corridor (geometry - untextured)

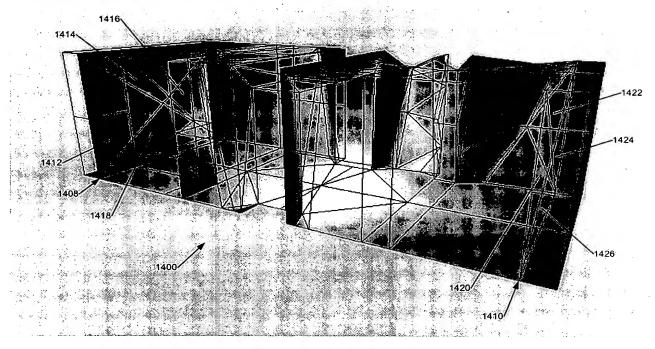


Figure 14a

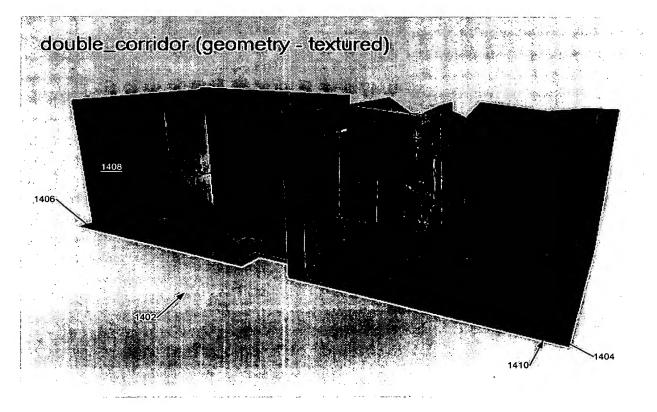


Figure 14b

double_corridor (plug geometry B_1 B_2 and portals P_1 P_2)

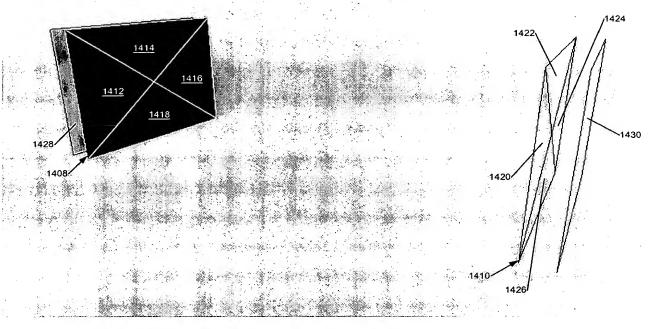


Figure 15a

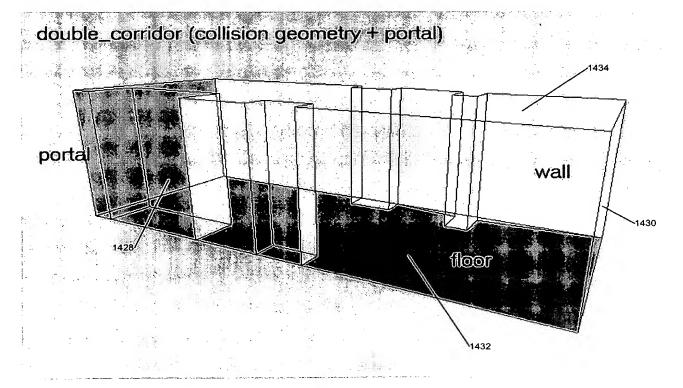


Figure 15b

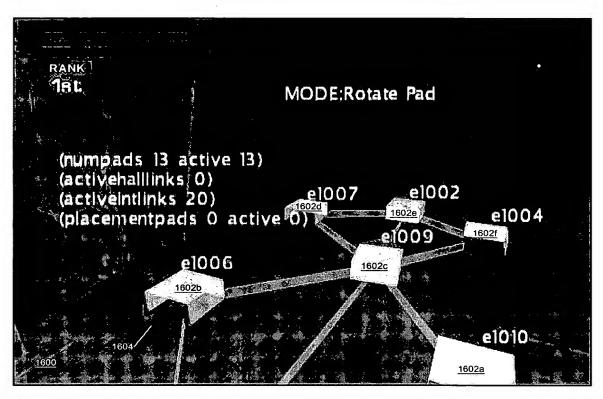


Figure 16a

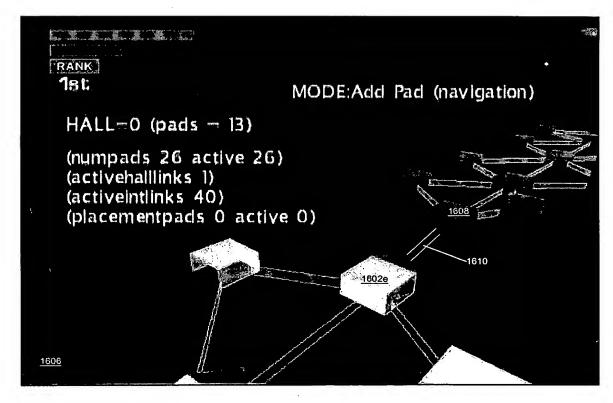


Figure 16b

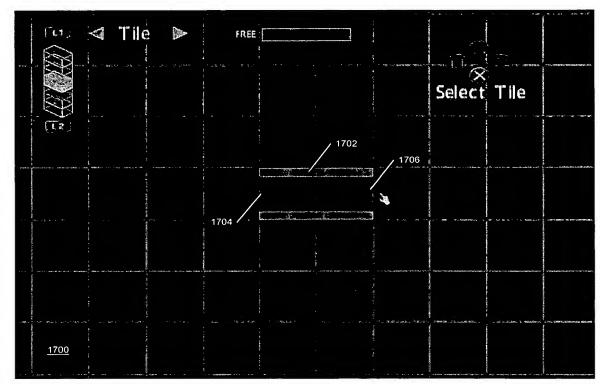


Figure 17a

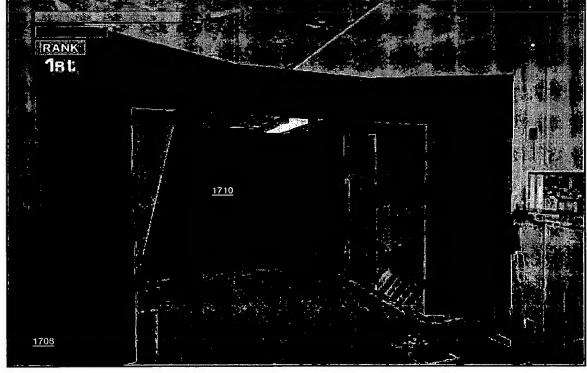


Figure 17b

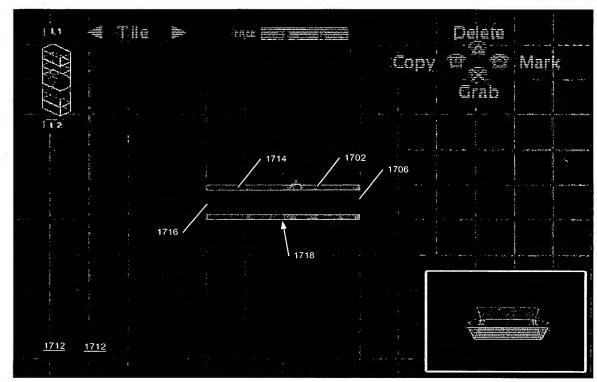


Figure 17c

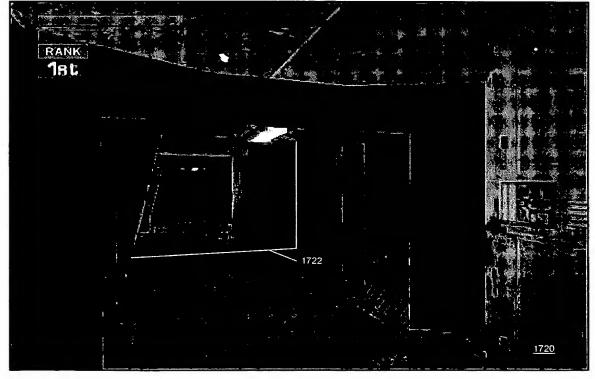


Figure 17d

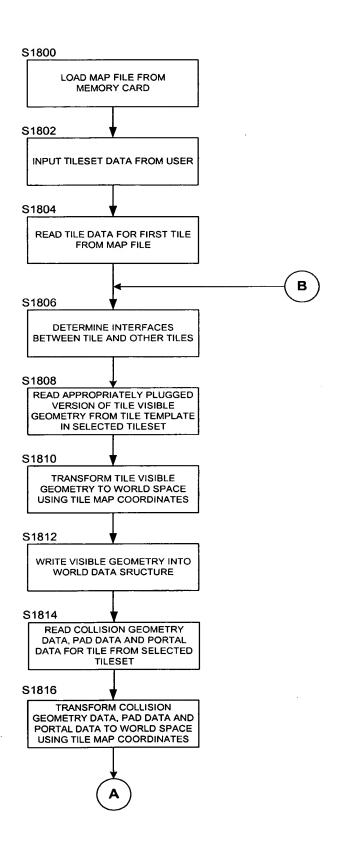


Figure 18a

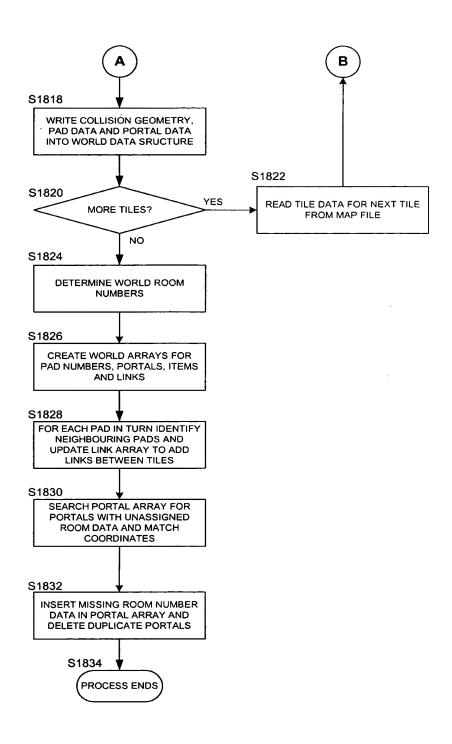


Figure 18b